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FIG. 3 shows in more detail how VDD and VSS are supplied from the core ring 4 to the core area 2. The core ring 4 comprises a pair of VDD lines 24 and a pair of VSS lines 26 disposed in a first metal layer (that is, a first layer of metal interconnection patterns), and a pair of VDD lines 28 and a pair of VSS lines 30 disposed in a second metal layer. A plurality of VDD lines 32 and VSS lines 34 extend across the core area 2 in the first metal layer. The VDD lines 24, 32 in the first metal layer are electrically coupled at their ends to the VDD lines 28 in the second metal layer by contacts. The VSS lines 26, 34 in the first metal layer are similarly coupled to the VSS lines 30 in the second metal layer. Core circuits such as the inverter 11 in FIG. 2 receive power from the VDD lines 32 and VSS lines 34 routed above them in the first metal layer.